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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/437,135	11/10/1999	SHUNPEI YAMAZAKI	0756-2064	7576

7590 06/28/2004
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EXAMINER

KIELIN, ERIK J

ART UNIT PAPER NUMBER

2813

DATE MAILED: 06/28/2004

Please find below and/or attached an Office communication concerning this application or proceeding.



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APPLICATION NO./ CONTROL NO.	FILING DATE	FIRST NAMED INVENTOR / PATENT IN REEXAMINATION	ATTORNEY DOCKET NO.
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EXAMINER

ART UNIT	PAPER
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20040624

DATE MAILED:

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Commissioner for Patents

DETAILED ACTION

Response to Amendment

The amendment filed on 14 June 2004 canceling all claims drawn to the elected invention and presenting only claims drawn to a non-elected invention is non-responsive (MPEP § 821.03). The remaining claims are not readable on the elected invention for the reason indicated below:

In the Amendment filed 14 June 2004, Applicant canceled all elected claims and submitted new claims drawn to a completely different invention --totally unrelated to the first inventions. The first inventions filed, as made clear from the first restriction requirement filed 21 November 2000, were

- I. Claims 1-14 are drawn to **fabrication of a semiconductor film and crystallizing using laser light.**
- II. Claims 15-28 are drawn to **fabrication of a semiconductor film and crystallizing by adding a metal catalyst and then heating.** (Emphasis added.)

And in the second restriction requirement filed 10 December 2003 were,

- I. Forming TFTs using a single **laser annealing step.**
- II. Forming TFTs using **two laser annealing steps.** (Emphasis added.)

By contrast the new claims are drawn to formation of a semiconductor device having a **pixel electrode** wherein the focus is the **materials** and **methods** of forming the **wiring structures** --not the semiconductor film. For example, new claim 38 recites,

- “38. (New) A method for manufacturing a semiconductor device comprising the steps of:
 - forming a semiconductor film over a substrate;
 - forming a **wiring** electrically connected to the semiconductor film;
 - and
 - forming a **pixel electrode** electrically connected to the **wiring**, wherein the **wiring includes a first layer comprising chromium and a second layer comprising aluminum thereon,**

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wherein the **first layer and the second layer are continuously formed by sputtering,**
wherein the first layer is in contact with the semiconductor film, and
wherein the **pixel electrode** is in contact with the first layer.”
(Emphasis added.)

This claim and claims 39-57 have nothing to do with using laser annealing to crystallize silicon for the formation of thin film transistors, and accordingly is completely different from the originally filed claims.

Note, should Applicant incorporate the above limitations along with the limitations in the presently canceled claims 1-37, these claims too will be restricted by original presentation as being drawn to a combination not previously claimed and which would have been properly restricted had they been originally presented.


Since the above-mentioned amendment appears to be a *bona fide* attempt to reply, applicant is given a TIME PERIOD of ONE (1) MONTH or THIRTY (30) DAYS, whichever is longer, from the mailing date of this notice within which to supply the omission or correction in order to avoid abandonment. EXTENSIONS OF THIS TIME PERIOD UNDER 37 CFR 1.136(a) ARE AVAILABLE.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Erik Kielin whose telephone number is 571-272-1693. The examiner can normally be reached on 9:00 - 19:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl Whitehead, Jr. can be reached on 571-272-1702. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Erik Kielin
Primary Examiner
24 June 2004